

CLK-GEN
ICS954310
Page 2

HOST 133/166MHz
PCIE 100MHz
VGA 96MHz
USB 48MHz
PCI 33MHz
REF 14MHz

3V / 5V
Page 24

+3VPCU
+5VPCU
+3V_S5
+5V_S5
+3VSUS
+5VSUS
+3V
+5V
+12VOUT

1.8V / 1.05V
Page 26

+1.8VSUS
SMDDR_VTERM
+1.05V

2.5V
Page 27

+2.5V

1.5V
Page 28

+1.5V

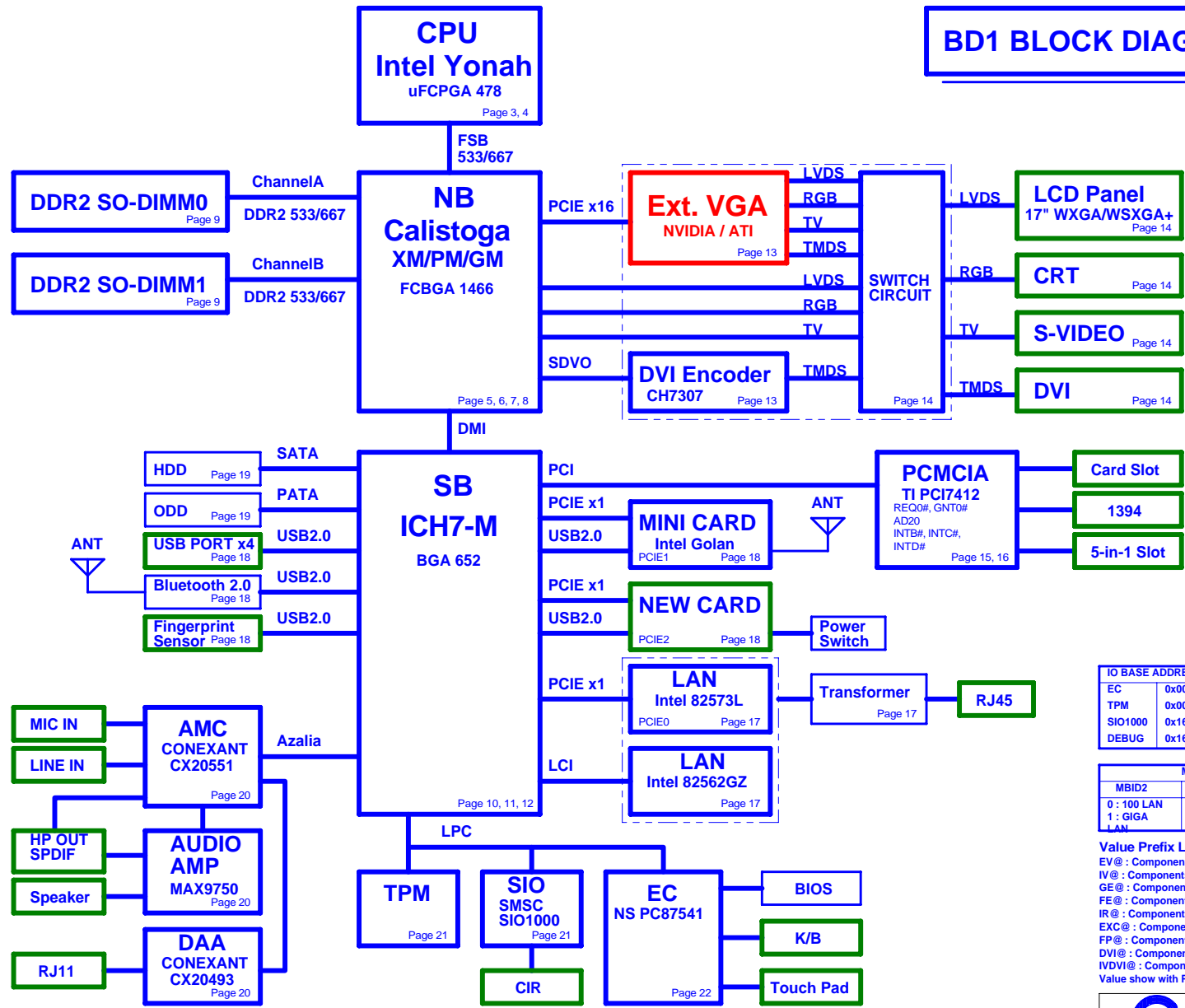
CPU CORE
Page 25

+VCC_CORE

BATTERY CHARGER
Page 29

VIN

Power State Table		
Power Name	Control Signal	Power State
+3VPCU	N/A	ALWAYS
+3V_S5	S5_ON	S0 - S5
+3VSUS	SUSON	S0 - S3
+3V	MAINON	S0
+5VPCU	N/A	ALWAYS
+5V_S5	S5_ON	S0 - S5
+5VSUS	SUSON	S0 - S3
+5V	MAINON	S0
+12VOUT	MAINON	S0
+1.8VSUS	SUSON	S0 - S3
SMDDR_VTERM	MAINON	S0
+1.05V	MAINON	S0
+1.5V	MAINON	S0
+2.5V	MAINON	S0
+VCC_CORE	VRON	S0



IO BASE ADDRESS	
EC	0x002E
TPM	0x004E
SIO1000	0x162E
DEBUG	0x164E

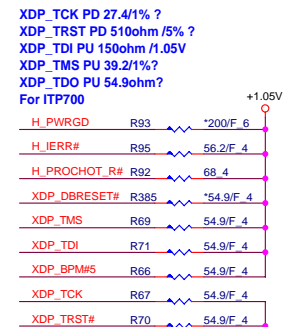
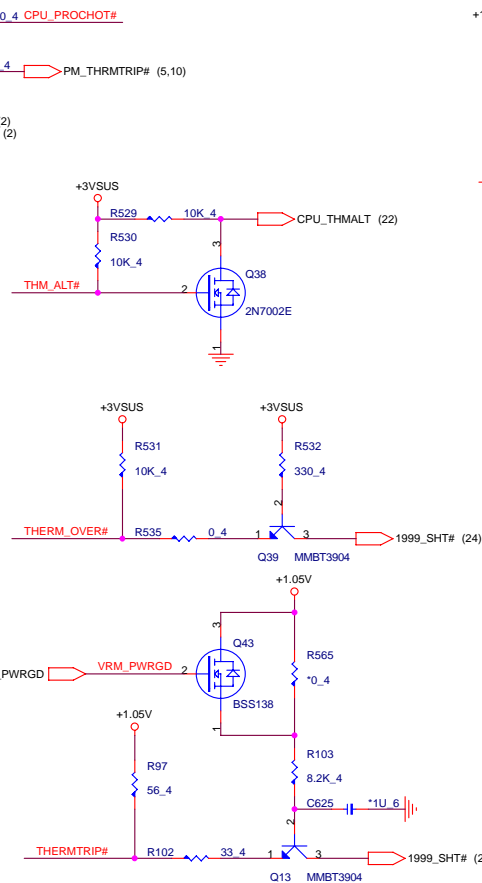
MB ID Definition		
MBID2	MBID1	MBID0
0 : 100 LAN	Reserved	Reserved
1 : GIGA LAN		

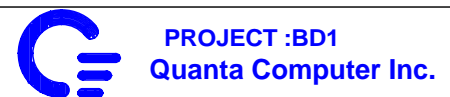
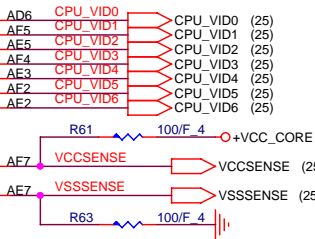
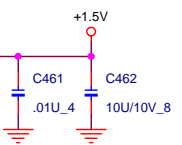
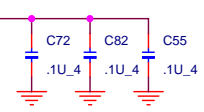
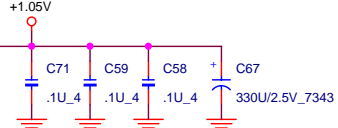
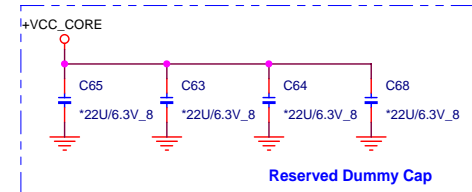
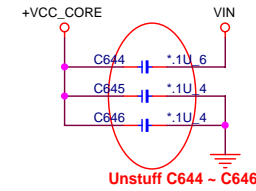
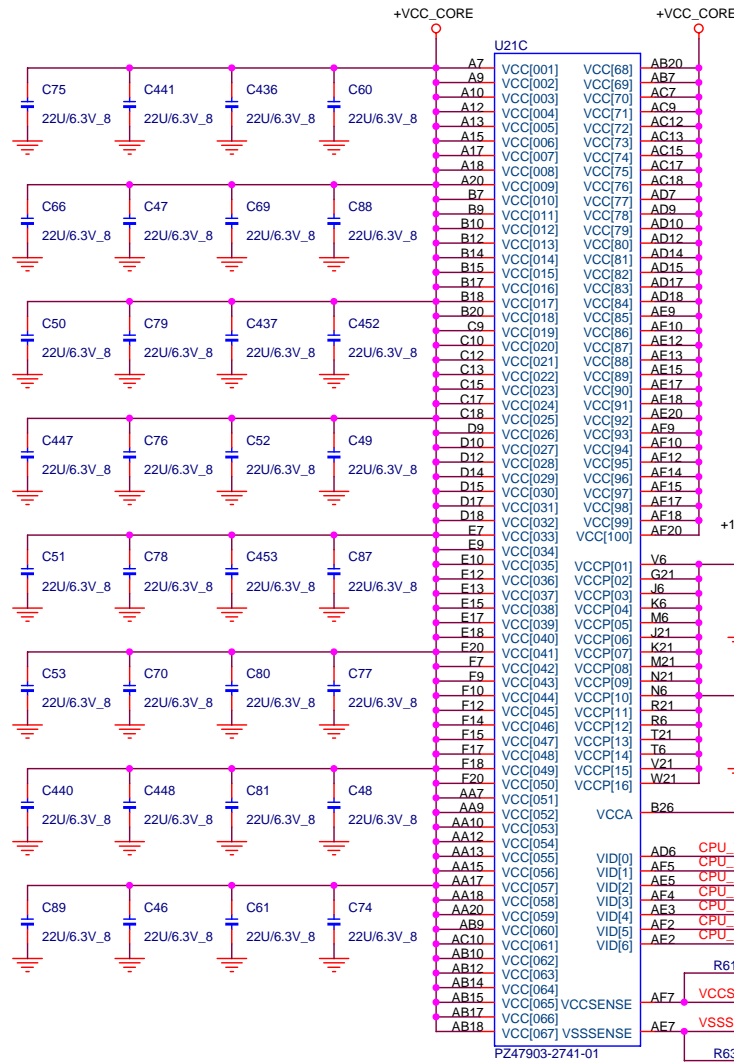
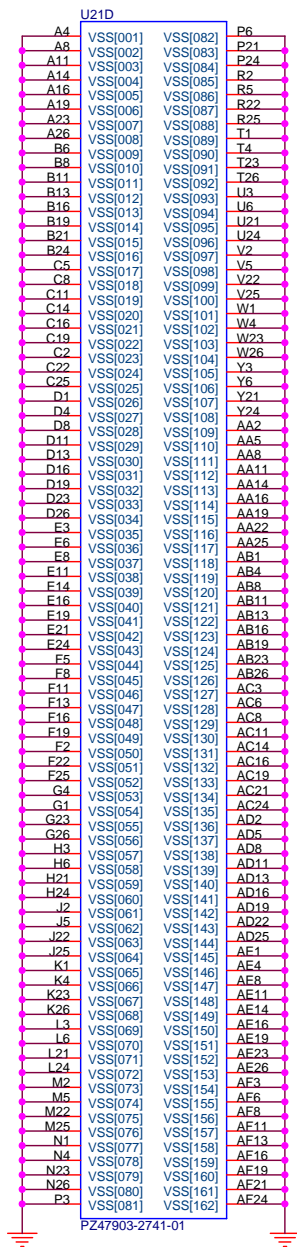
Value Prefix List :

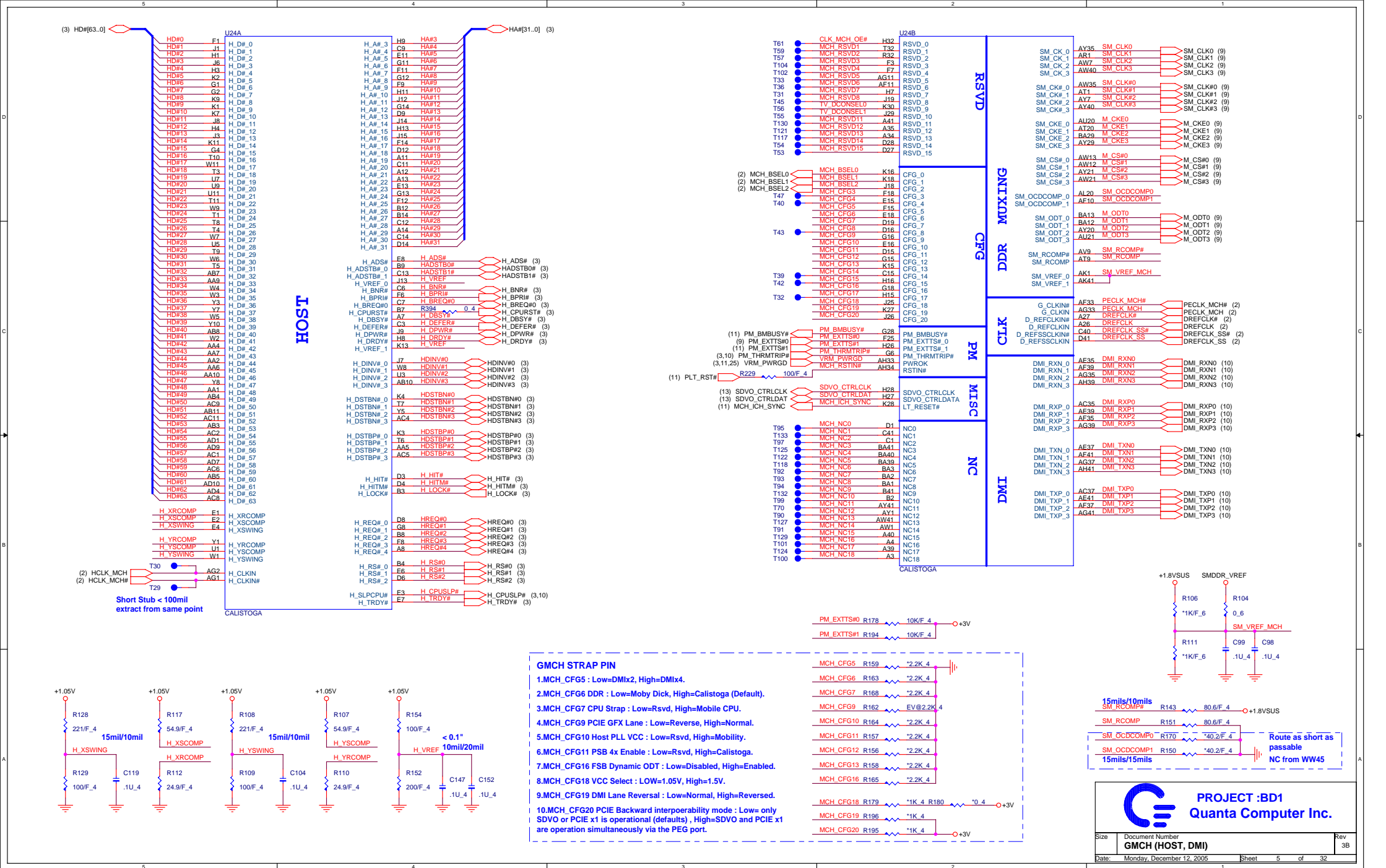
EV@ : Components For External VGA
IV@ : Components For Internal VGA
GE@ : Components For Gigabit Ethernet
FE@ : Components For Fast Ethernet
IR@ : Components For CIR
EXC@ : Components For ExpressCard
FP@ : Components For FingerPrint
DVI@ : Components For DVI Function
IVDVI@ : Components For Internal VGA DVI Function
Value show with Prefix " * " means unstuff parts

PROJECT :BD1
Quanta Computer Inc.

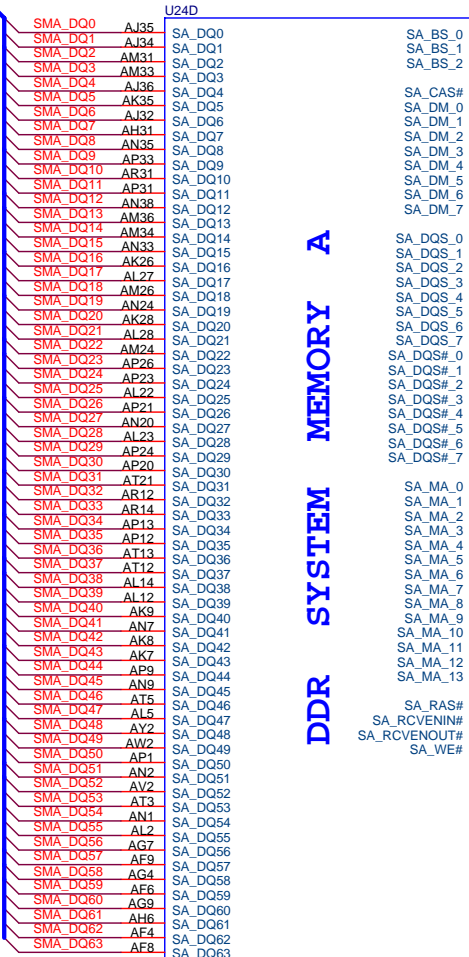
Size	Document Number	Rev
	BLOCK DIAGRAM	3B
Date	Monday, December 12, 2005	Sheet 1 of 32







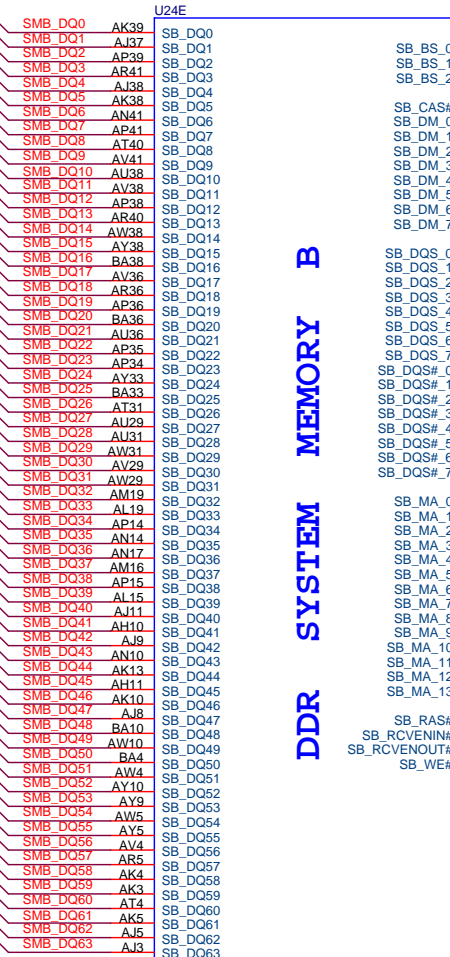
(9) SMA_DQ[63..0]



DDR SYSTEM MEMORY A

CALISTOGA

(9) SMB_DQ[63..0]



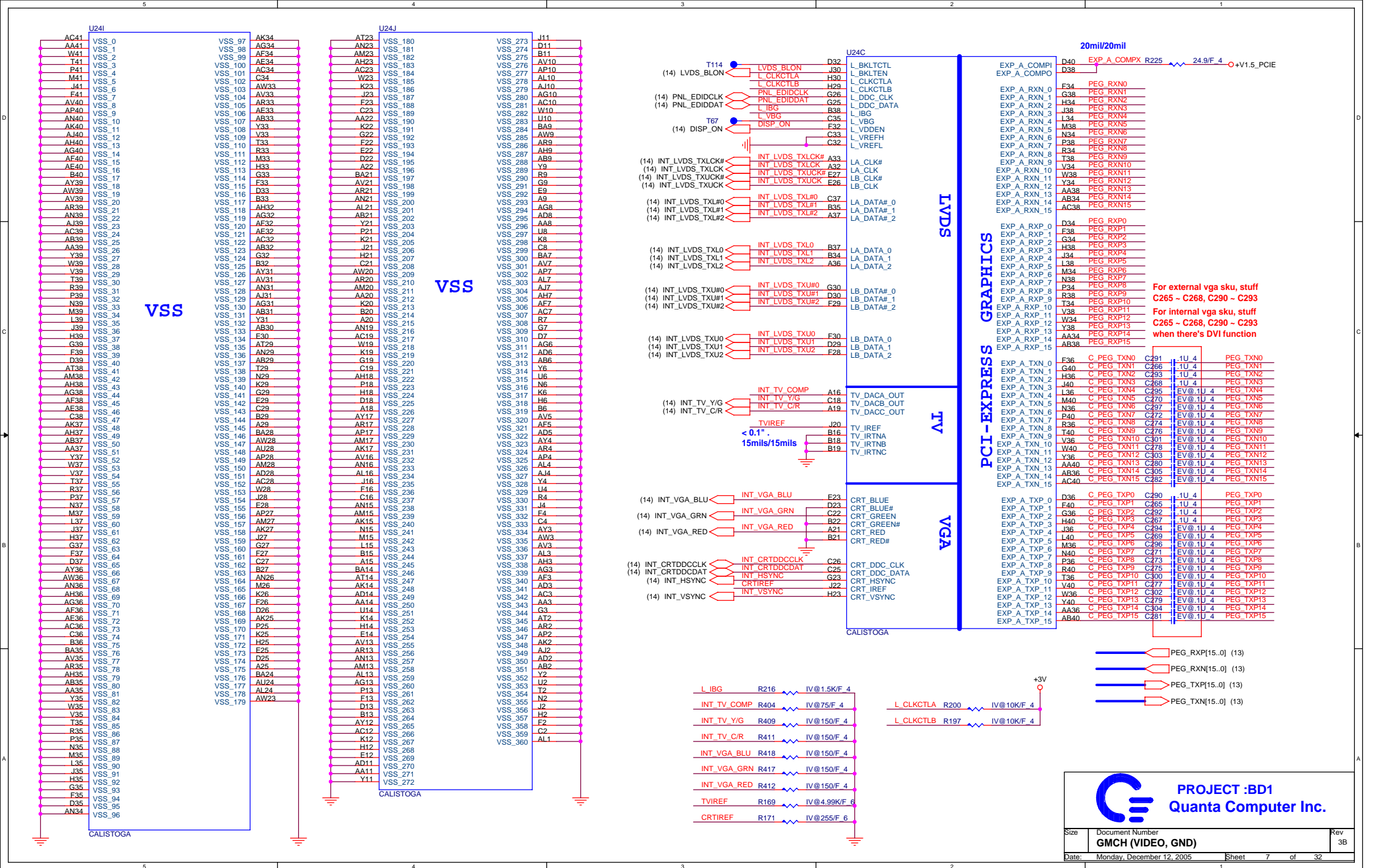
DDR SYSTEM MEMORY B

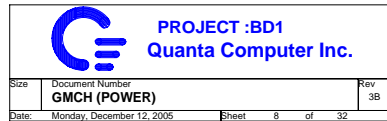
CALISTOGA

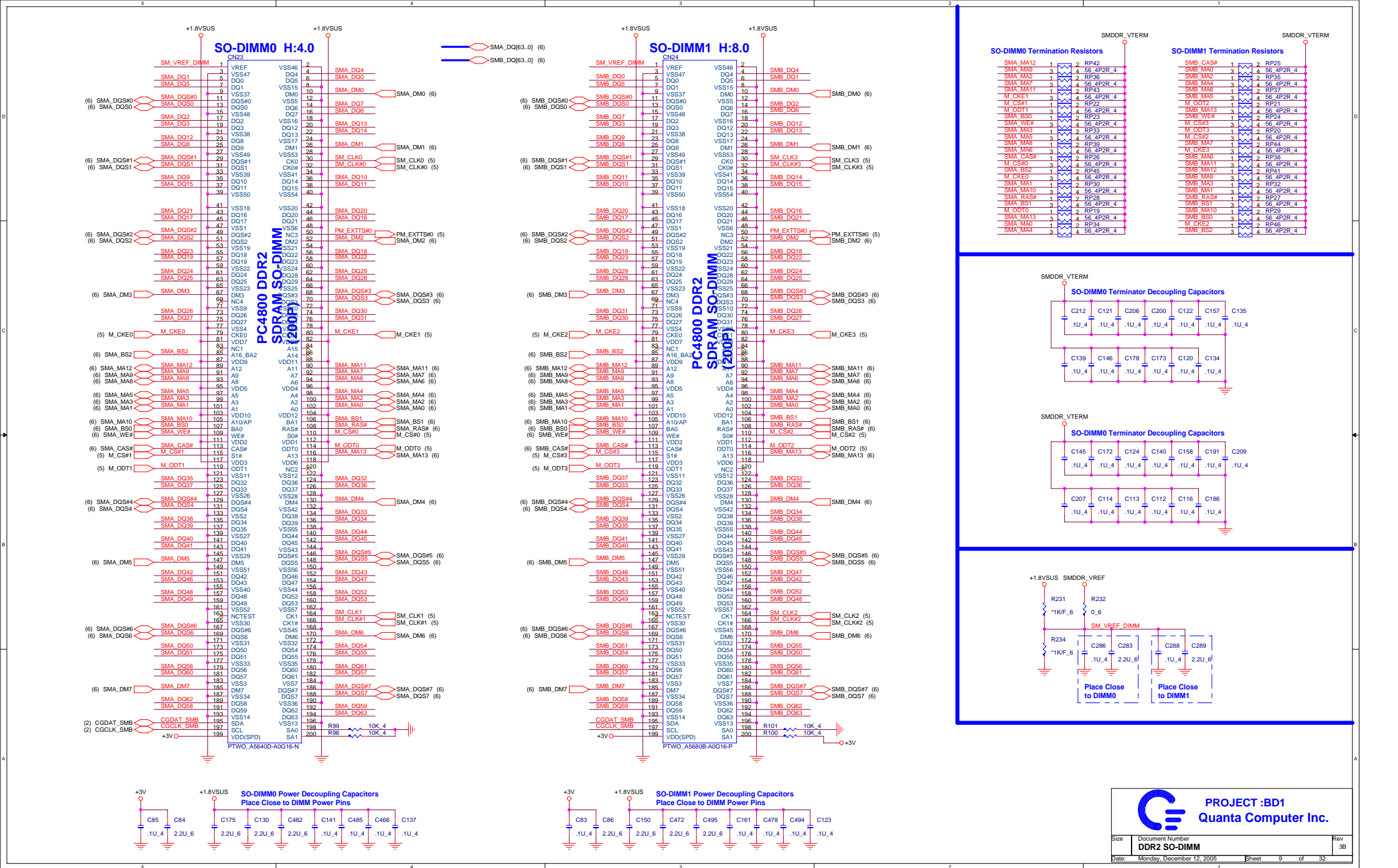


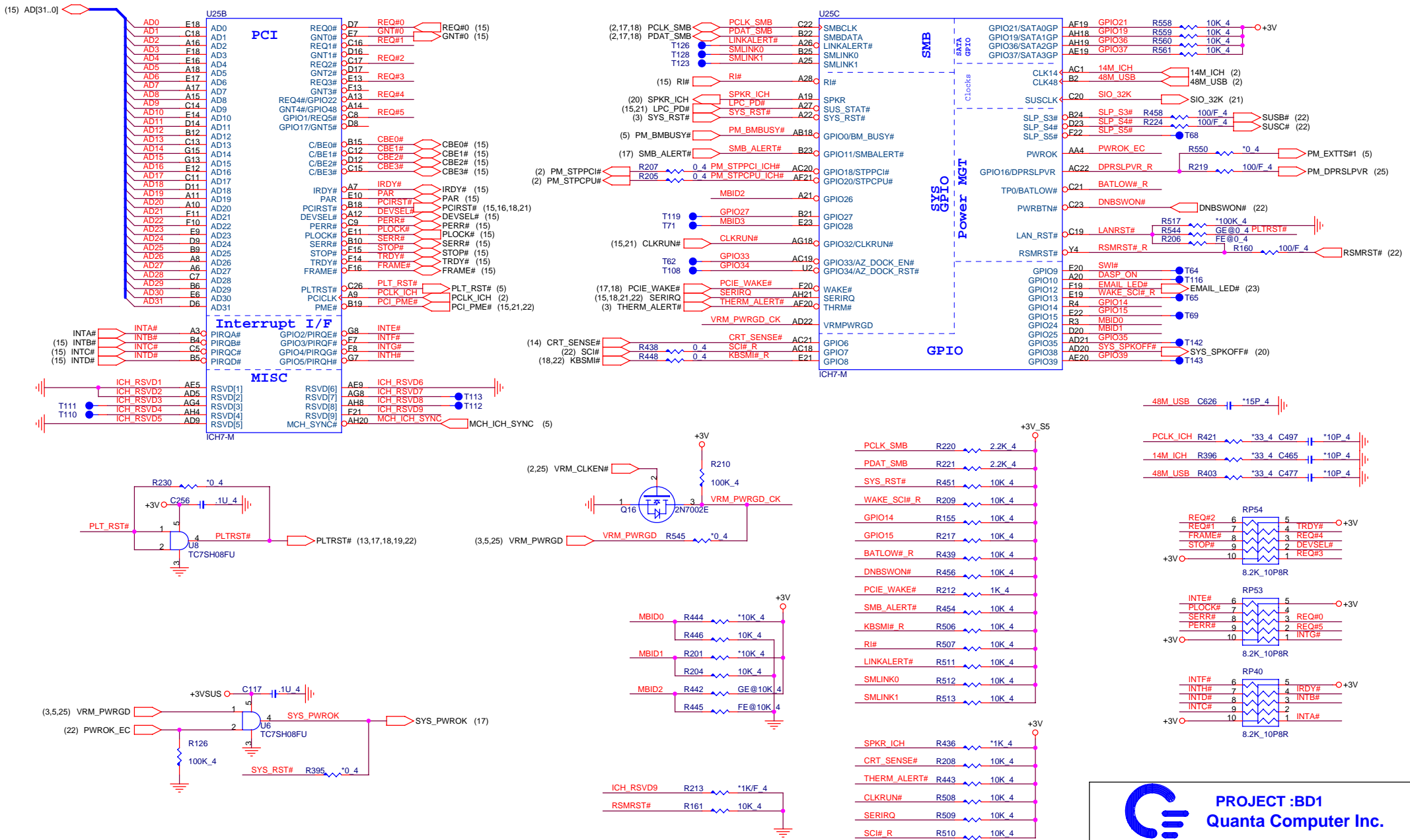
PROJECT :BD1
Quanta Computer Inc.

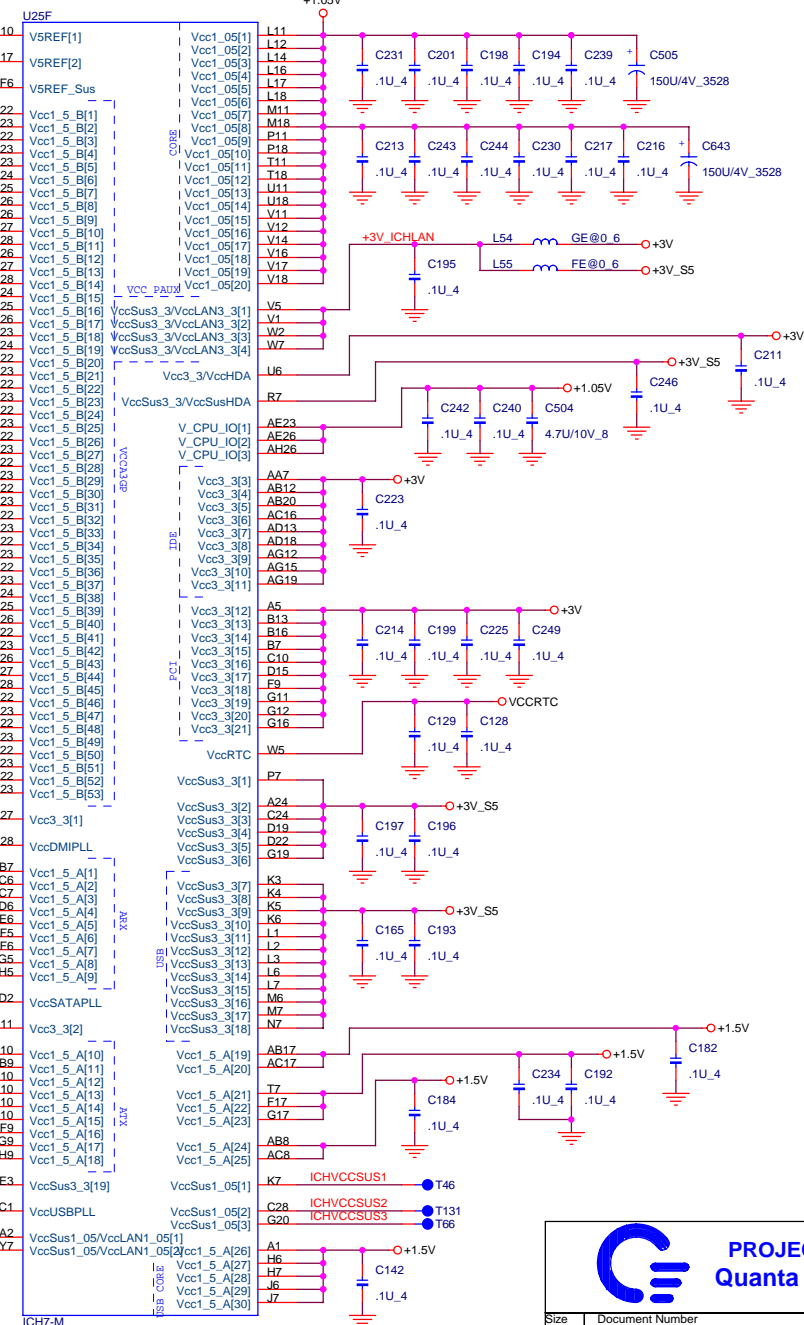
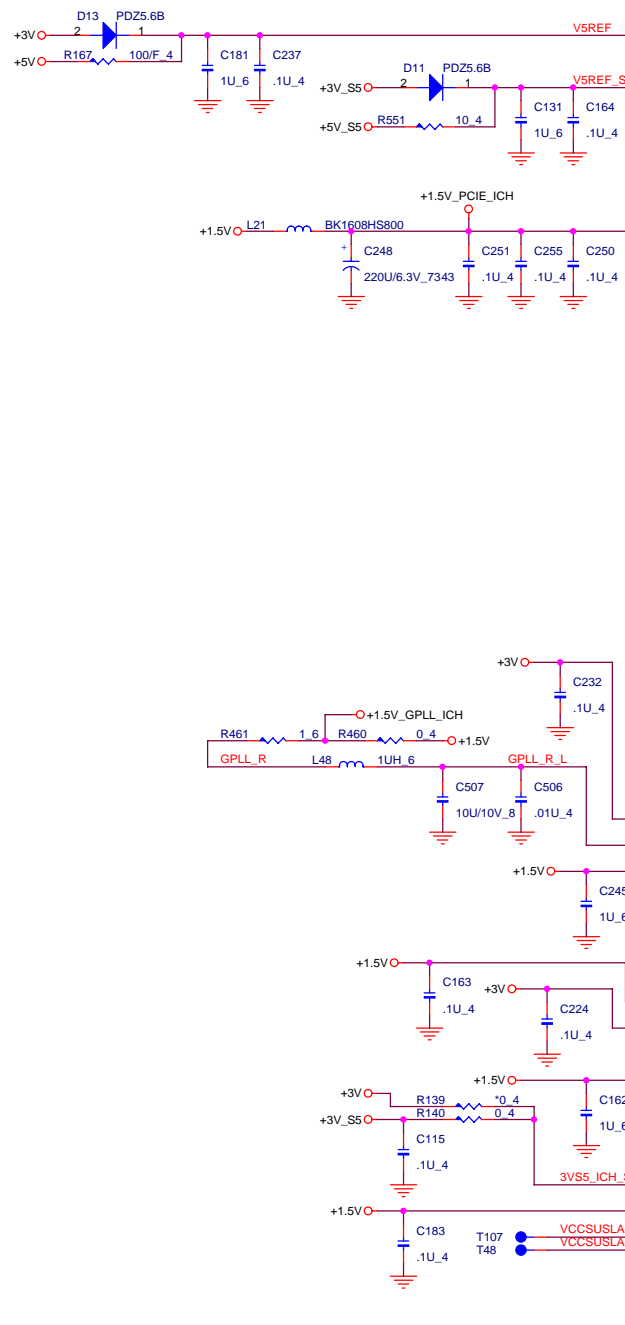
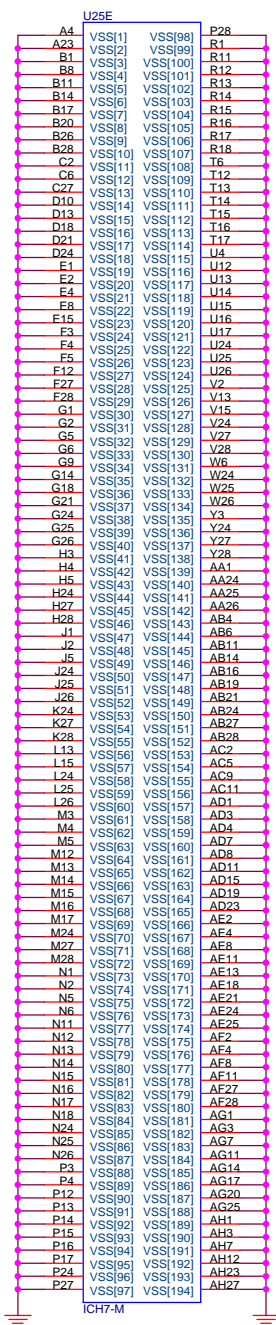
Size	Document Number	Rev
	GMCH (MEMORY)	3B
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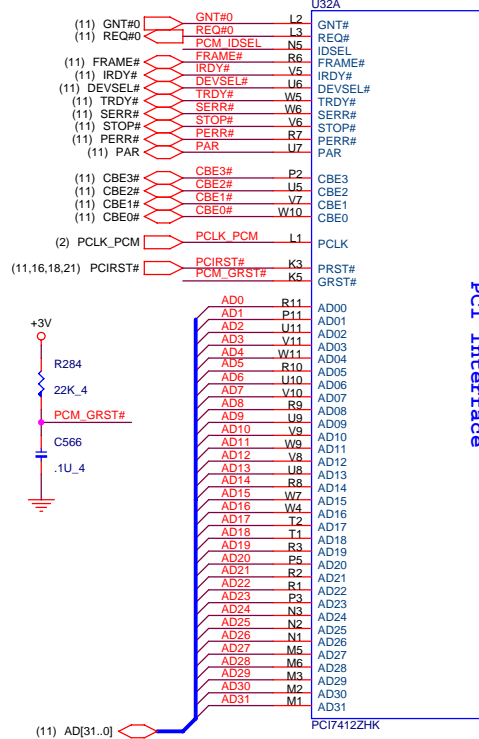


PROJECT :BD1
Quanta Computer Inc.

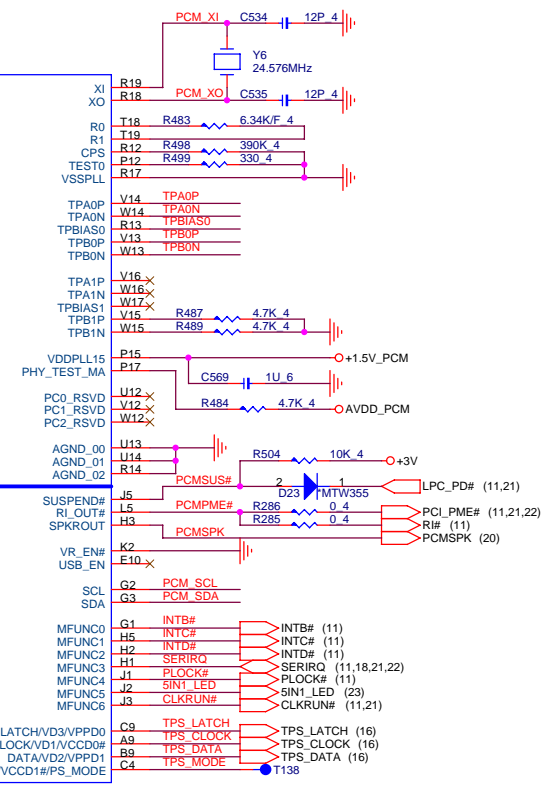
12/12 Unstuff DVI function for UMA Sku



AD20 R502 150 4 PCM_IDSEL
PCLK_PCM R505 *33 4 C567 *10P 4



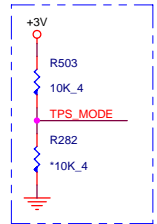
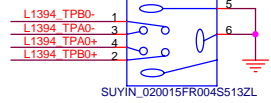
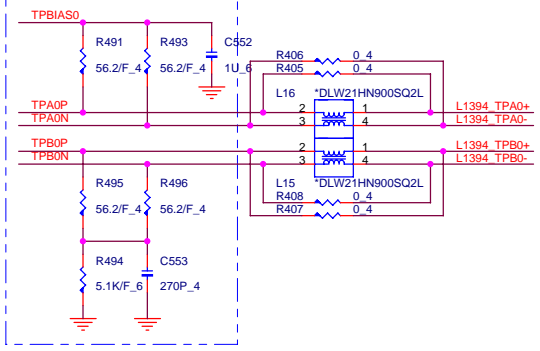
1394 Interface
Miscellaneous



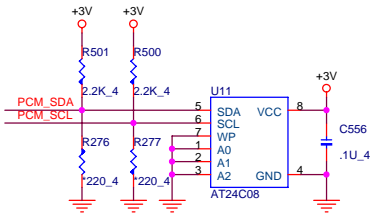
U32 change p/n : AJ084120T08 --> AJ074120T13

1394

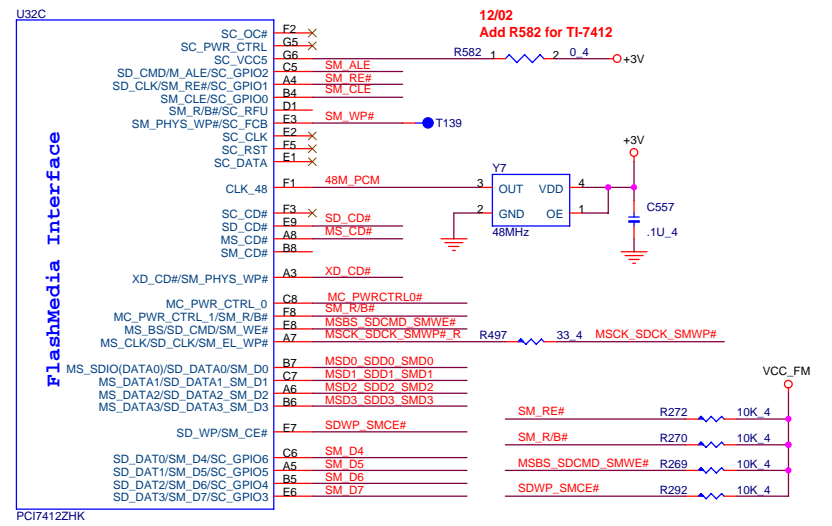
Place Close to Chip Side



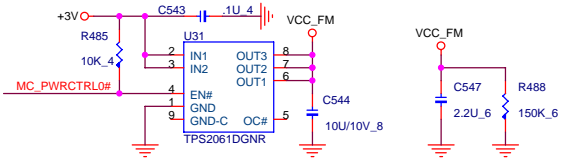
TPS_MODE :
Power switch select
H : Serial I/F
L : Parallel I/F



5-IN-1 Card Reader

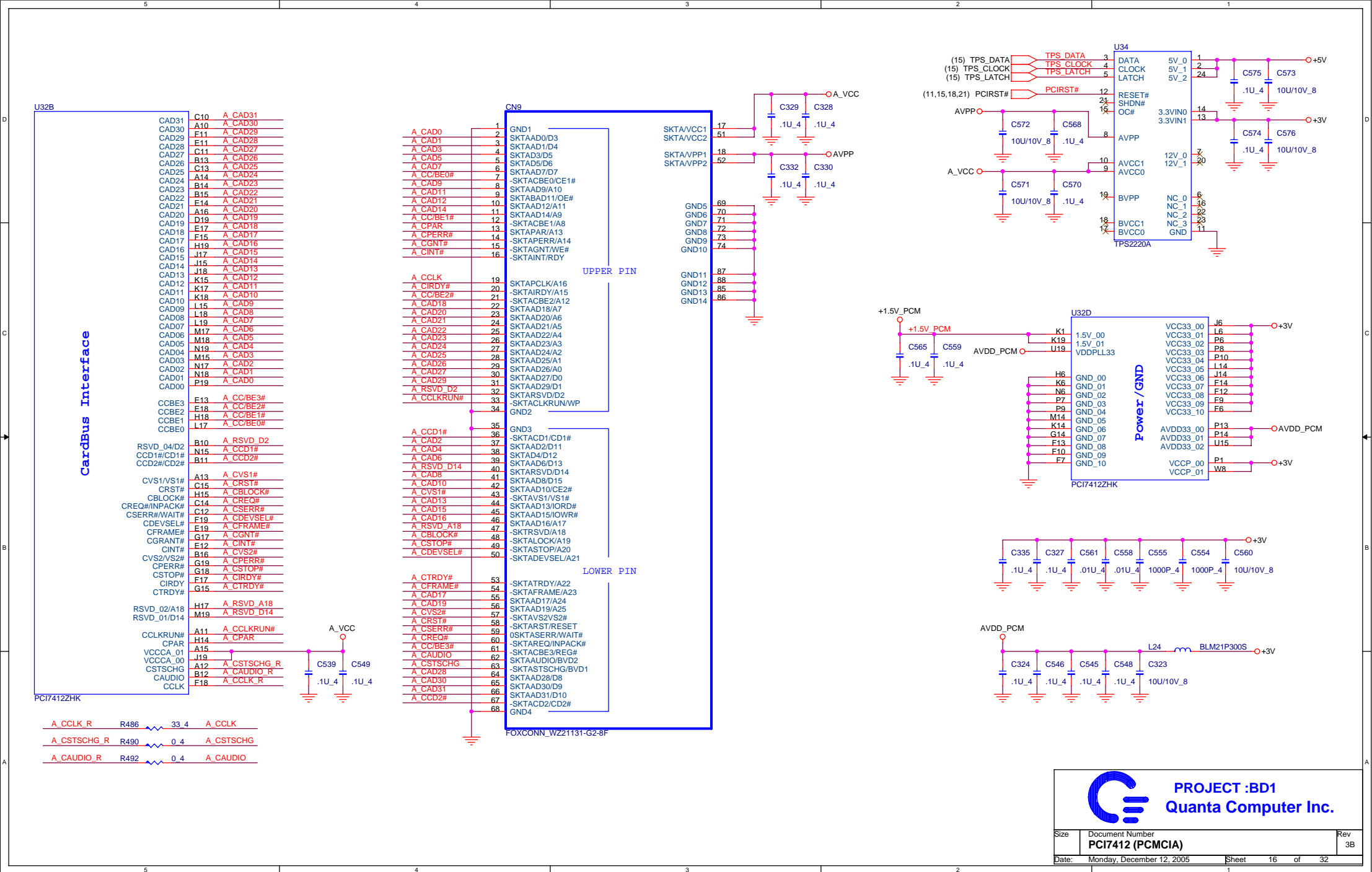


MSBS_SDCMD_SMWE#	2	MS_BS	xD_CD	23	XD_CD#
MS_CD#	6	MS_INS	xD_R/#	25	SM_R/#
MSCK_SDCK_SMWP#	8	MS_SCLK	xD_RE	26	SM_RE#
MSD0_SDD0_SMD0	4	MS_DATA0	xD_CE	27	SDWP_SMCE#
MSD1_SDD1_SMD1	3	MS_DATA1	xD_CLE	28	SM_CLE
MSD2_SDD2_SMD2	5	MS_DATA2	xD_ALE	29	SM_ALE
MSD3_SDD3_SMD3	7	MS_DATA3	xD_WE	30	MSBS_SDCMD_SMWE#
			xD_WP	31	MSCK_SDCK_SMWP#
MSBS_SDCMD_SMWE#	13	SD_CMD	xD_D0	33	MSD0_SDD0_SMD0
MSCK_SDCK_SMWP#	16	SD_CLK	xD_D1	34	MSD1_SDD1_SMD1
MSD0_SDD0_SMD0	19	SD_DATA0	xD_D2	35	MSD2_SDD2_SMD2
MSD1_SDD1_SMD1	20	SD_DATA1	xD_D3	36	MSD3_SDD3_SMD3
MSD2_SDD2_SMD2	11	SD_DATA2	xD_D4	37	SM_D4
MSD3_SDD3_SMD3	12	SD_DATA3	xD_D5	38	SM_D5
SD_CD#	21	SD_CD_SW	xD_D6	39	SM_D6
SDWP_SMCE#	43	SD_WP_SW	xD_D7	40	SM_D7
	22	SD_CD_COM			
	44	SD_WP_COM			

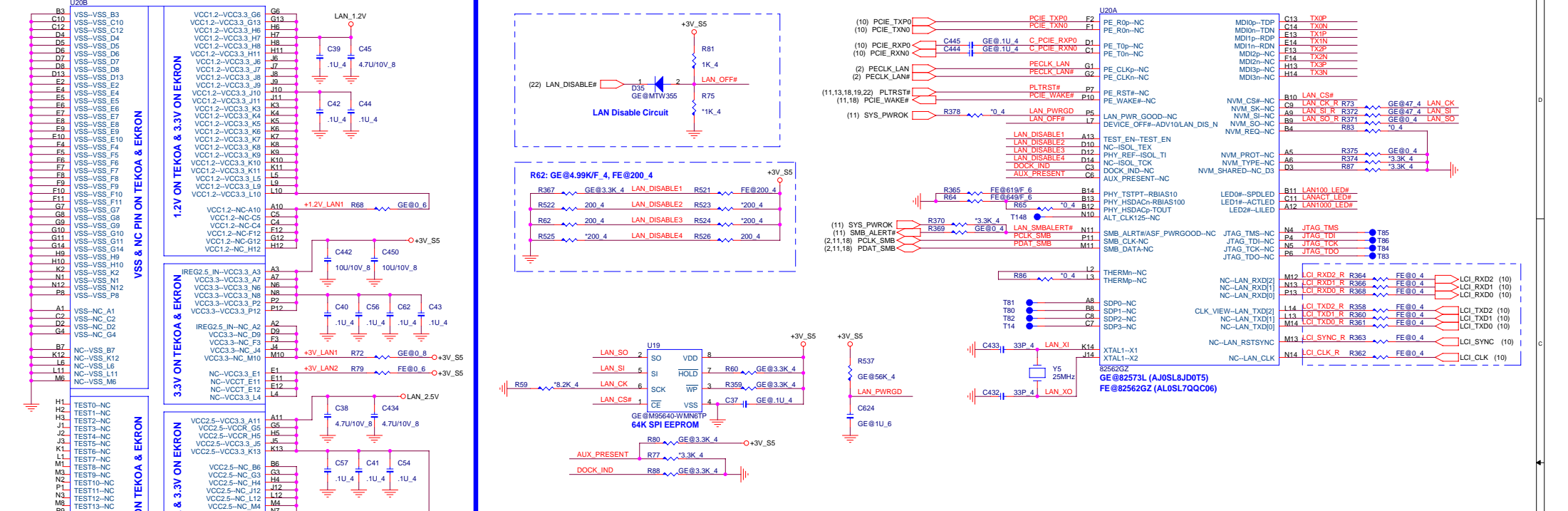


PROJECT :BD1
Quanta Computer Inc.

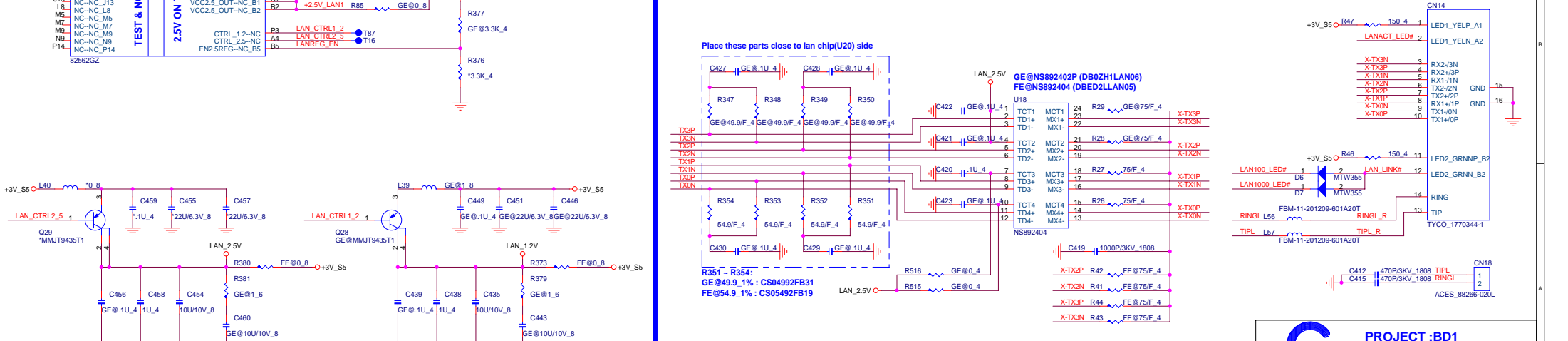
Size	Document Number	Rev
	PCI7412 (1394, 5IN1)	3B
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POWER

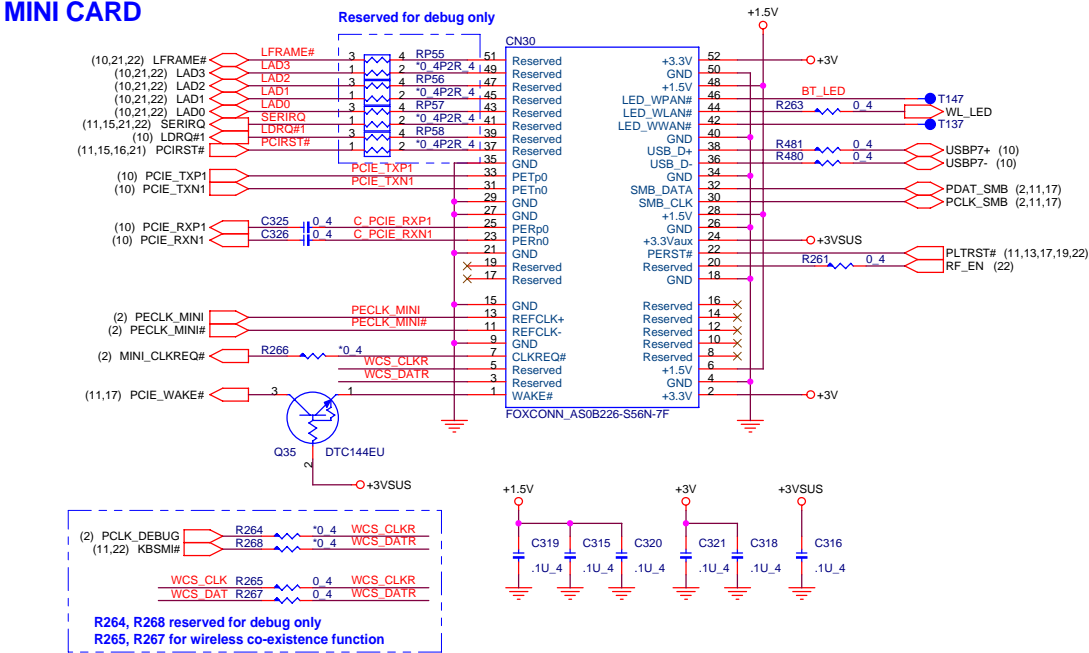


TRANSFORMER & RJ45

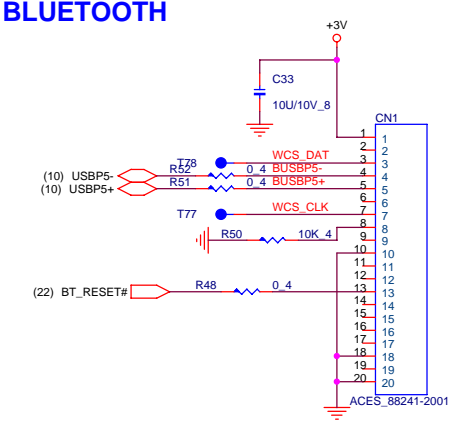


		PROJECT :BD1 Quanta Computer Inc.	
Size	Document Number	Rev	
	LAN (82573L/82562GZ)	3B	
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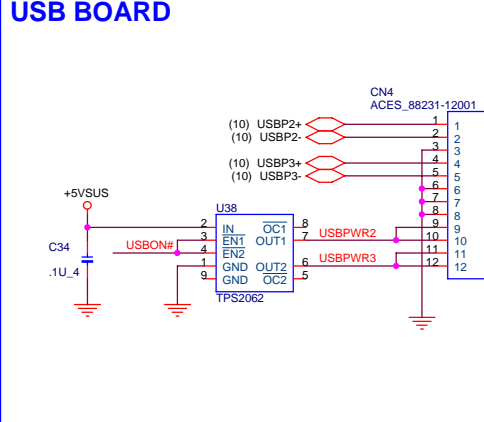
MINI CARD



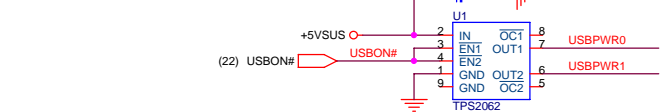
BLUETOOTH



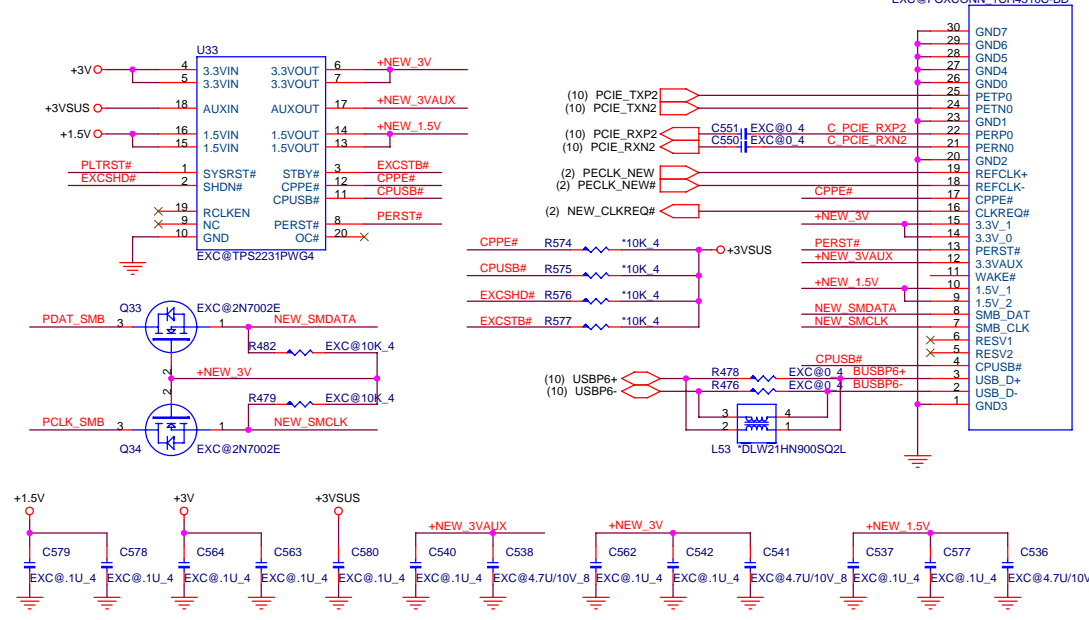
USB BOARD



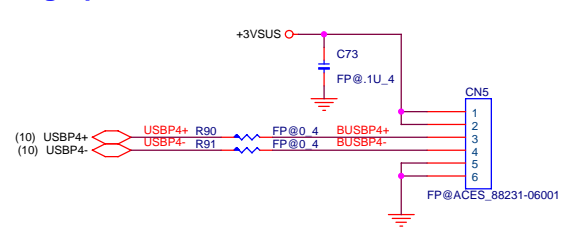
USB PORT x2




NEW CARD



Fingerprint

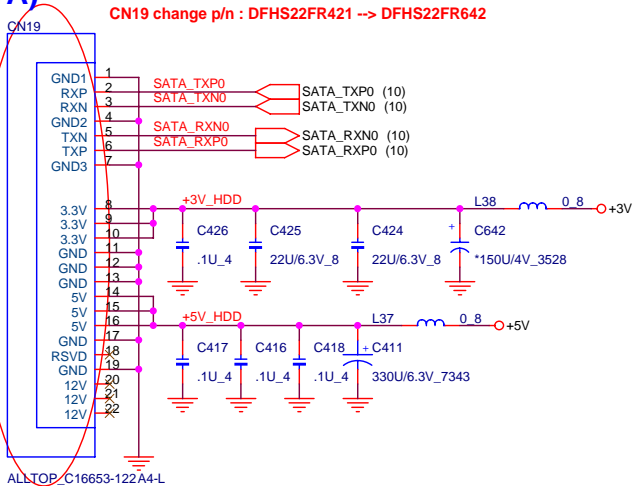




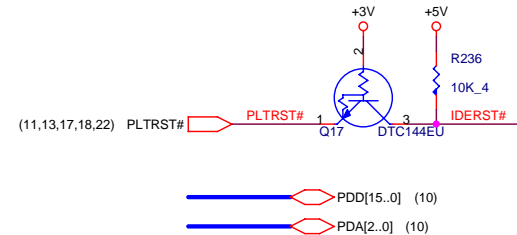
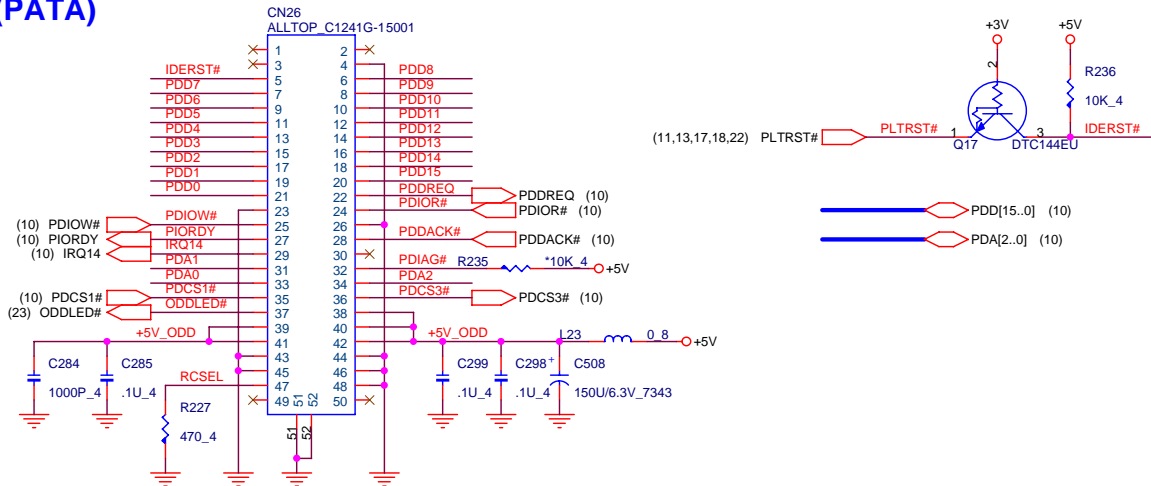
PROJECT :BD1
Quanta Computer Inc.

Size	Document Number	Rev
	MINI CARD/NEW CARD/USB	3B
Date:	Monday, December 12, 2005	Sheet 18 of 32

HDD (SATA)



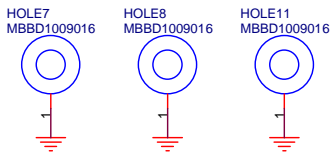
ODD (PATA)



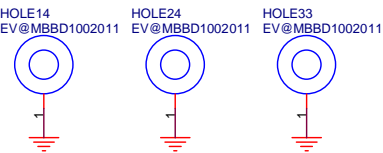
12/12 Unstuff PAD6 and stuff
PAD1 for modem cable clip

SCREW HOLE

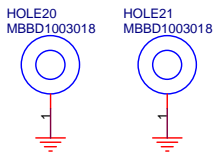
CPU



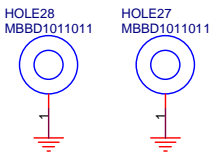
VGA BOARD



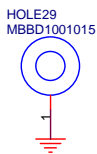
MINI CARD



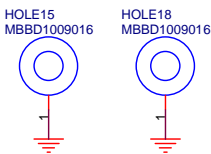
MODEM



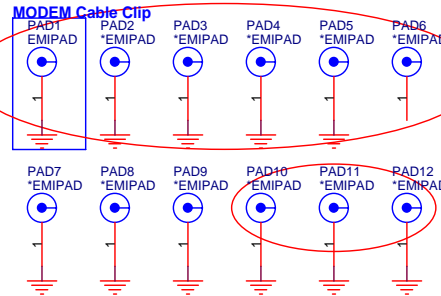
AUDIO BOARD



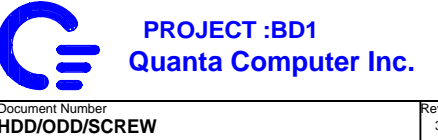
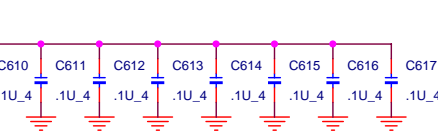
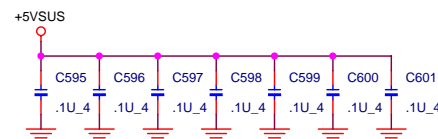
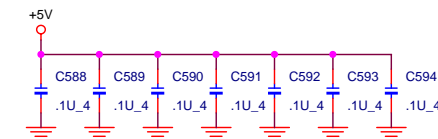
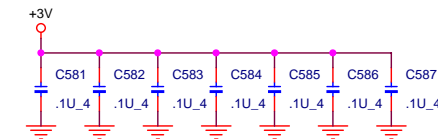
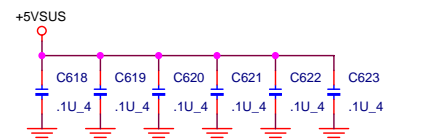
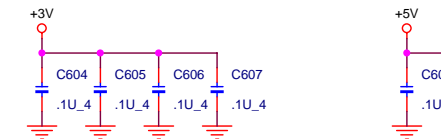
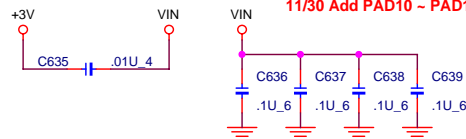
NB



EMI



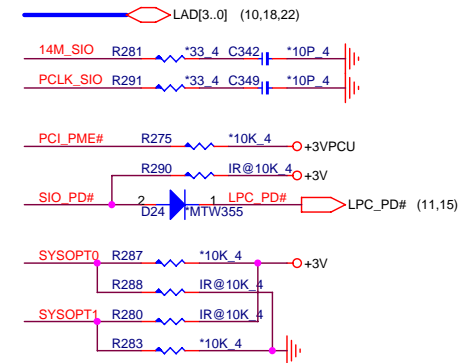
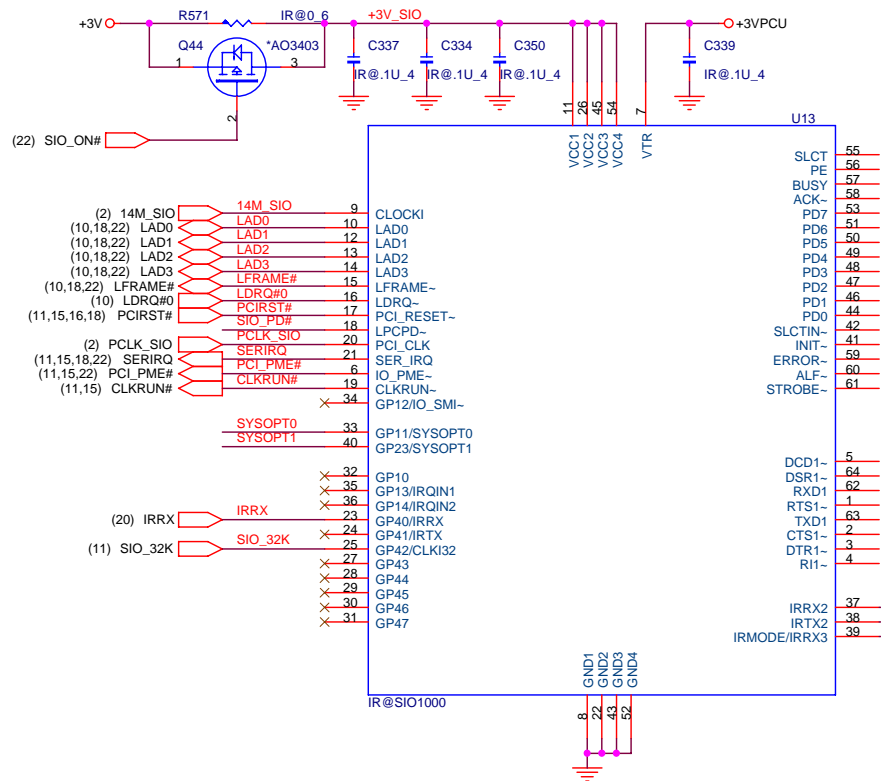
11/30 Add PAD10 ~ PAD12 for EMI



PROJECT :BD1
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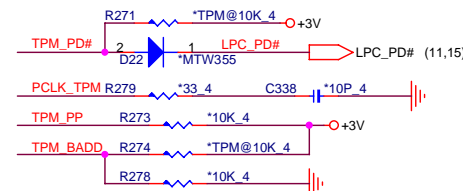
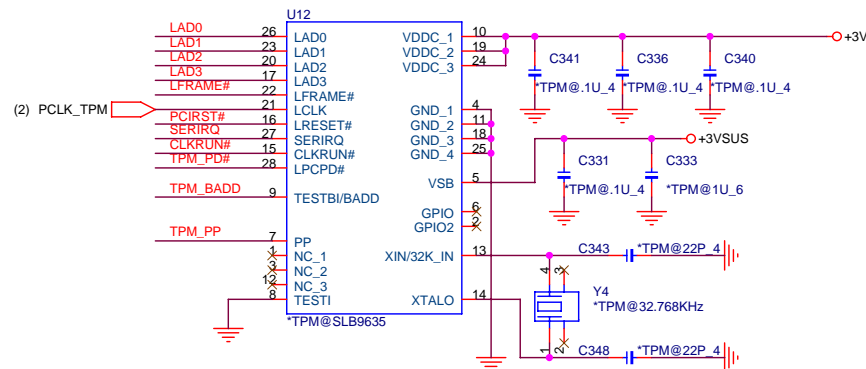
Size	Document Number	Rev
	HDD/ODD/SCREW	3B
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SIO



SYSOPT1	SYSOPT0	DEFAULT CONFIGURATION PORT BASE ADDRESS
0	0	0X002E
0	1	0X004E
1	0	0X162E
1	1	0X164E

TPM 1.2



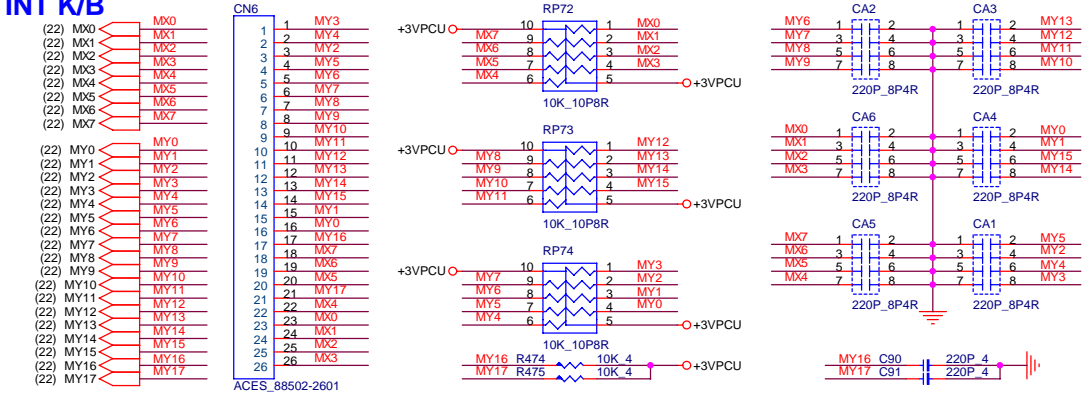
TPM_PP	HI : ACCESS LOW : NORMAL (Internal PD)
TPM_BADD	HI : 4E/4F h LOW : 2E / 2F h



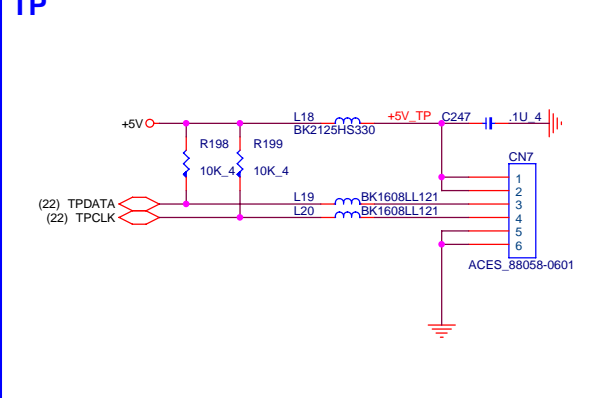
PROJECT :BD1
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Size	Document Number	Rev
	SIO1000/TPM	3B
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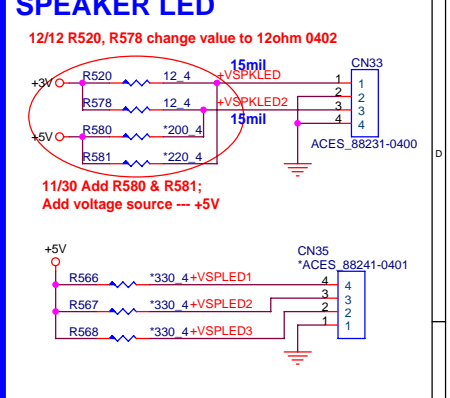
INT K/B



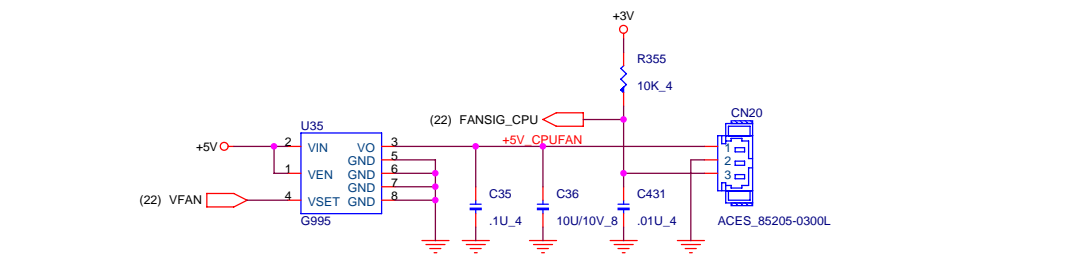
TP



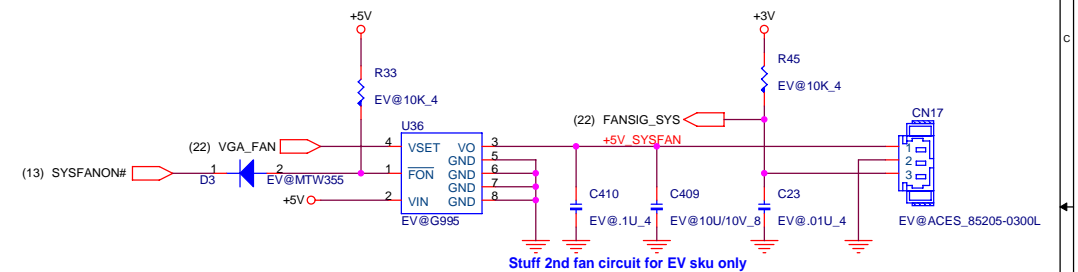
SPEAKER LED



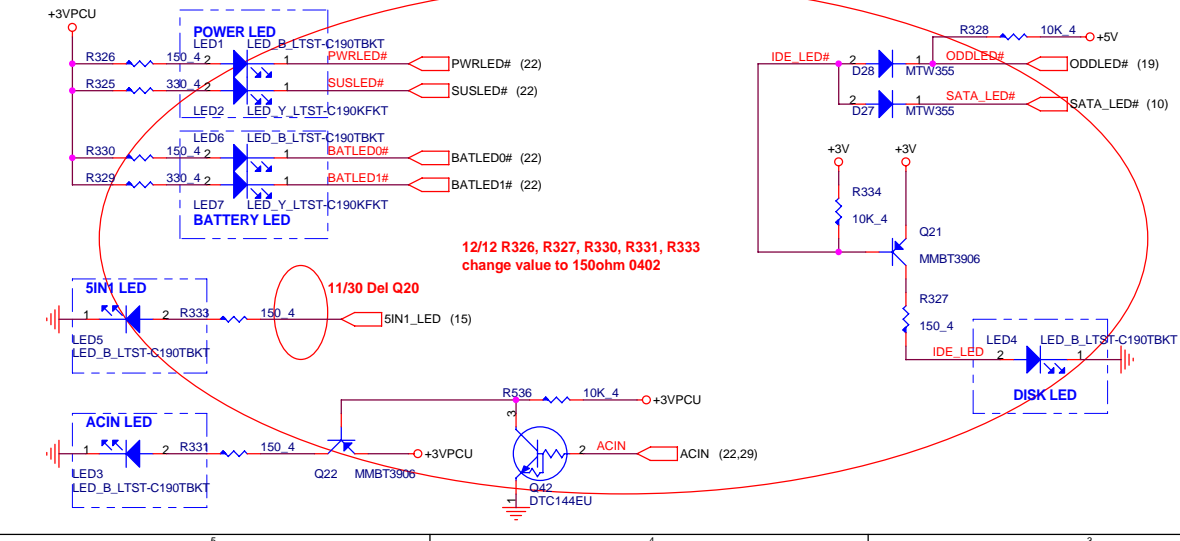
CPU FAN



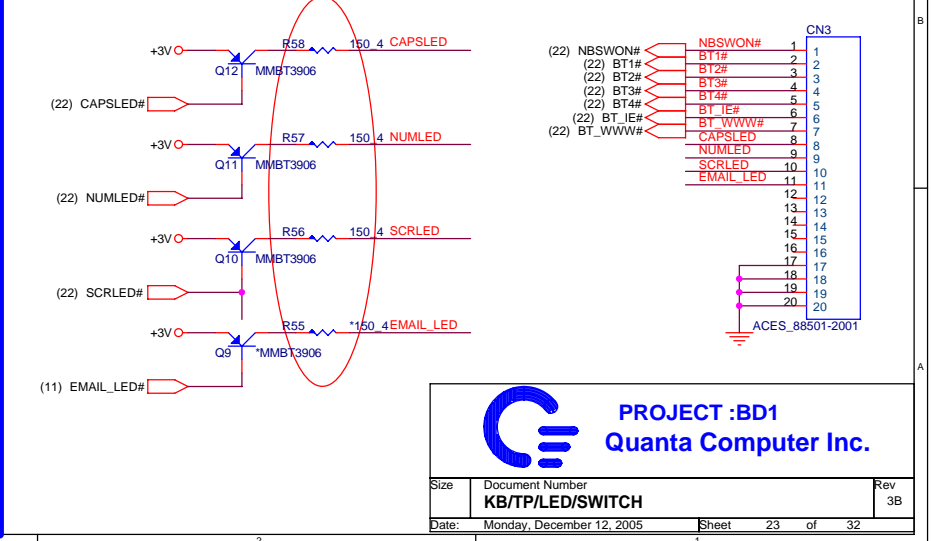
2ND FAN




LED



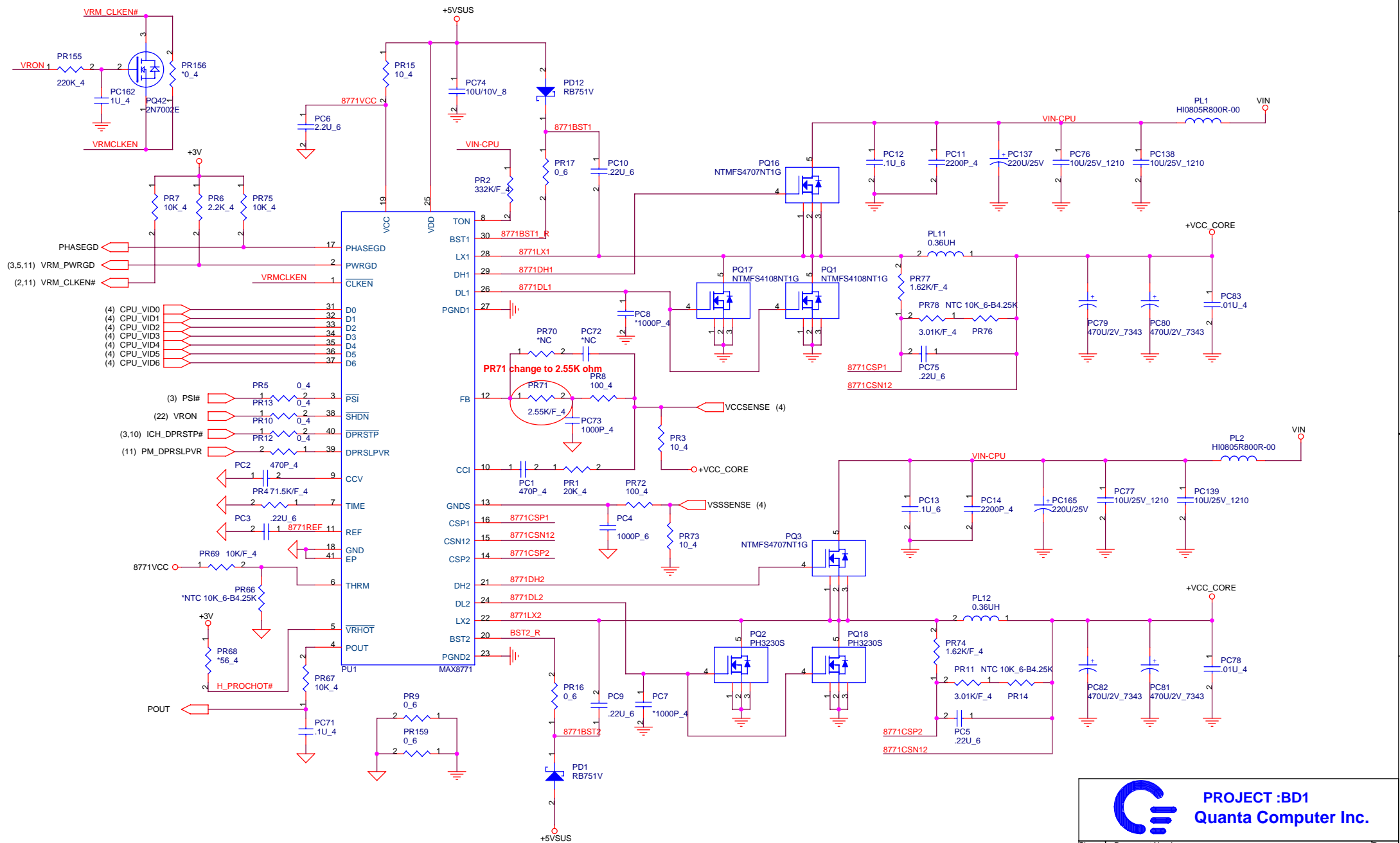
BUTTON BOARD






PROJECT :BD1
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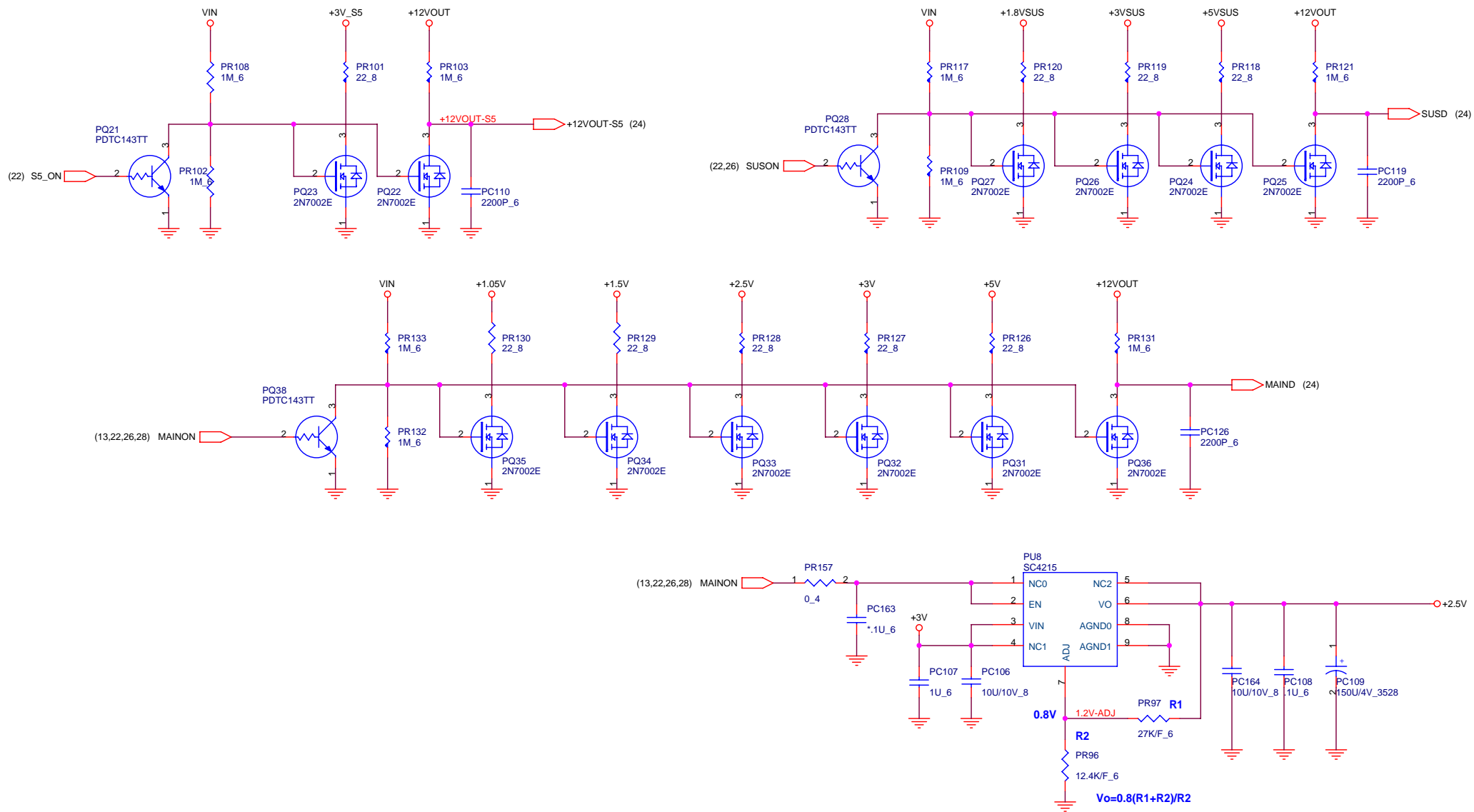
Size	Document Number	Rev
	KB/TP/LED/SWITCH	3B
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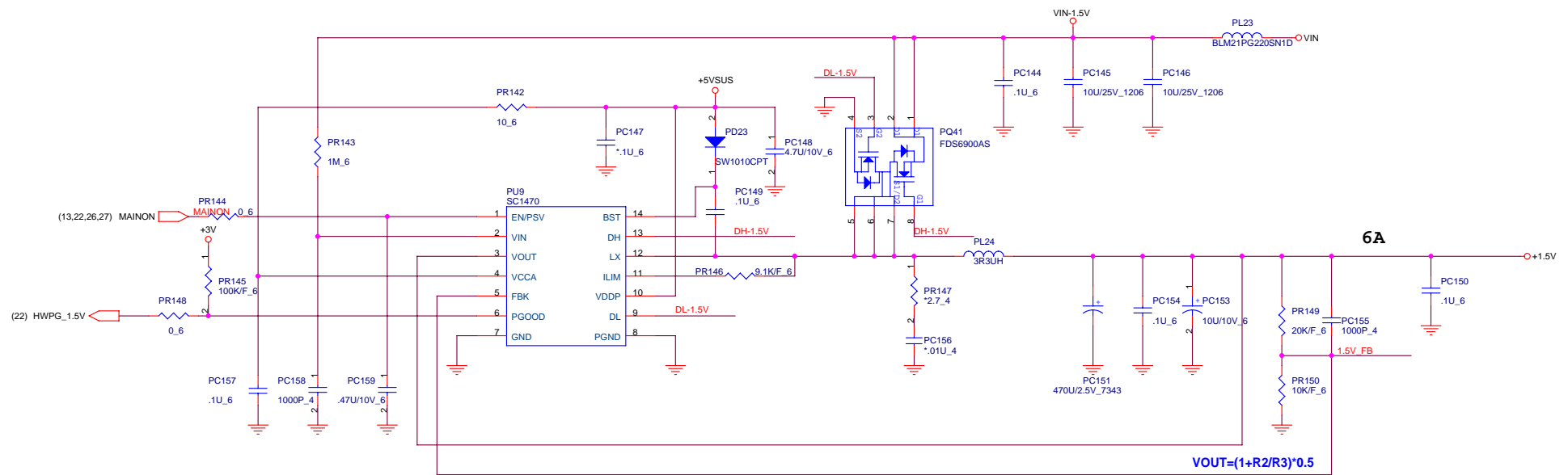
PROJECT :BD1
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Size	Document Number	Rev
	CPU CORE (MAX8771)	3B
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PROJECT :BD1
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Size	Document Number	Rev
	2.5V/DISCHARGE	3B
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PROJECT :BD1
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Size	Document Number	Rev
	+1.5V	3B
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[illegible]

MODEL:		REV:	CHANGE LIST:	MODEL : BD1 MB		
BD1 MotherBoard	3B	<p>Page2. Add C647~C653 to reserve.</p> <p>Page8. L14 package change to RC0805.</p> <p>Page15. Add R582 for TI-7412, in TI-7412 Pin-G6 is +3V power.</p> <p>Page19. Add PAD10~PAD12 for EMI.</p> <p>Page20. Del Q37,R528 --- not use.</p> <p>Net name change to Kill_LED from BT_LED</p> <p>Net name change to Kill_LED# from BT_LED#.</p> <p>Page23. Del Q20 for 5IN1 LED.</p> <p>Add R580 and R581to support speaker voltage source can be change to +5V.</p> <p>R520 and R578 change to 12ohm for speaker current (9~10mA).</p> <p>Page29. Add PC168 for EMI</p> <p>change Battery connector for 100W.</p>		PAGE	FROM	TO
				1	3A	3B
				2	3A	3B
				3	3A	3B
				4	3A	3B
				5	3A	3B
				6	3A	3B
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				29	3A	3B
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